LESSON PLAN:

Discipline: CSE & IT	Semester: 3rd	Name of the Teaching Faculty: Ms. Supriya Mishra
Subject: Computer System Architecture	No. Of Days per week class allotted: **4 periods** er week (Mon, Wed, Thurs = Fri-1 period each)	Semester: From Date:15-09-2022 To 22-12-2022

WEEK	CLASS DAY	THEORY /PRACTICAL TOPICS
1st	15-09-2022	Introductory Class & Interaction with new batch
	16-09-2022	Syllabus Discussion and Introduction to CSA
2nd	19-09-2022	**1. Basic structure of computer hardware** 1.1 Basic Structure of computer hardware
	21-09-2022	1.2 Functional Units
	22-09-2022	1.3 Computer components
	23-09-2022	1.4 Performance measures
3rd	26-09-2022	1.5 Memory addressing (Part 1)
	28-09-2022	1.5 Memory addressing(Part 2) & Operations
	29-09-2022	**Recap of Chapter 1** **2. Instructions & instruction Sequencing** 2.1 Fundamentals to instructions
	30-09-2022	2.2 Operands
4th	03-10-22 to 08-10-2022	Puja Holiday
5th	10-10-2022	2.3 Op Codes
	12-10-2022	2.4 Instruction formats
	13-10-2022	2.5 Addressing Modes (Part 1)
	14-10-2022	2.5 Addressing Modes (Part 2) **Class Test-1 (Tentative)**
6th	17-10-2022	**Recap of Chapter 2** **3. Processor System** 3.1 Register Files
	19-10-2022	3.2 Complete instruction execution (Part 1) • Fetch • Decode • Execution
	20-10-2022	3.2 Complete instruction execution (Part 2) **Assignment-1**
	21-10-2022	3.3 Hardware control 3.4 Micro program control
7th	26-10-2022	**Diff b/w Hardwired Control and Microprogrammed Control** **Recap of Chapter 3**
	27-10-2022	**4. Memory System** 4.1 Memory characteristics
	28-10-2022	4.2 Memory hierarchy
8th	31-10-2022	4.3 RAM and ROM organization (Part 1)

	Date	Topic
	02-11-2022	4.3 RAM and ROM organization (Part 2)
	03-11-2022	4.4 Interleaved Memory
	04-11-2022	4.5 Cache memory
9th	07-11-2022	4.6 Virtual memory
	09-11-2022	**Recap of Chapter 4** **5. Input – Output System** 5.1 Input - Output Interface
	10-11-2022	5.2 Modes of Data transfer 5.3 Programmed I/O Transfer 5.4 Interrupt driven I/O
	11-11-2022	5.5 DMA
10th	14-11-2022	**Internal Assessment (To be decided by Institution)**
	16-11-2022	5.6 I/O Processor
	17-11-2022	**Recap of Chapter 5** **6. I/O Interface & Bus architecture** 6.1 Bus and System Bus
	18-11-2022	6.2 Types of System Bus • Data Bus • Address Bus • Control Bus
11th	21-11-2022	**Assignment-2** 6.3 Bus Structure
	23-11-2022	6.4 Basic Parameters of Bus design
	24-11-2022	6.5 SCSI
	25-11-2022	6.6 USB
12th	28-11-2022	**Recap of Chapter 6** **7. Parallel Processing** **Introduction to Parallel Processing** 7.1 Parallel Processing
	30-11-2022	7.2 Linear Pipeline
	01-12-2022	7.3 Multiprocessor
	02-12-2022	7.4 Flynn"s Classification
13th	05-12-2022	**Recap of Chapter 1**
	07-12-2022	**Recap of Chapter 2**
	08-12-2022	**Recap of Chapter 2(Addressing Modes)**
	09-12-2022	**Recap of Chapter 3**
14th	12-12-2022	**Recap of Chapter 4**
	14-12-2022	**Recap of Chapter 4 (Cache Memory)**
	15-12-2022	**Recap of Chapter 4 (Virtual Memory)**
	16-12-2022	**Recap of Chapter 5**
15th	19-12-2022	**Recap of Chapter 6**
	21-12-2022	**Recap of Chapter 7**
	22-12-2022	Last Date for submission of Notes by students