

Discipline: ME IT	Semester: 4 th	No. Of period available	Name of the Teaching Faculty: Bhabodeepika Mohanty (PTGF)
Subject: MPMC	No. Of Days/per week class allotted: 5 periods per week Mon, Tue, Wed, Thu, Sat		From Date: 14-02-2023 To Date: 23 -05-2023 No. Of Weeks: 15 weeks
Week	Class date		Topics to be covered
1 st	14.02.23	1	Unit 1 : Microprocessor (Architecture & Programming -8 bit 8085) 1.1 Introduction to Microprocessor and Microcomputer
	15.02.23	1	1.1 Introduction to Microprocessor and Microcomputer & distinguish Between them. 1.2 Concept of Address bus, data bus, control bus
	16.02.23	1	1.2 System Bus 1.3 General Bus structure Block diagram.
2 nd	20.02.23	1	1.4 Basic Architecture of 8085 (8 bit) Microprocessor
	21.02.23	1	1.5 Signal Description (Pin diagram) of 8085 Microprocessor
	22.02.23	1	1.6 Register Organizations, Distinguish between SPR & GPR
	23.02.23	1	1.6 Timing & Control Module,
	25.02.23	1	1.7 Stack, Stack pointer & Stack top.
3 rd	27.02.23	1	1.8 Interrupts:-8085 Interrupts,
	28.02.23	1	Masking of Interrupt(SIM,RIM)
	01.03.23	1	Assignment 1 Cum Doubt Clearing
	02.03.23	1	Unit-2: Instruction Set and Assembly Language Programming 2.1 Addressing data & Differentiate between one-byte, two-byte & three -byte instructions with Examples.
	04.03.23	1	2.2 Addressing modes in instructions with suitable examples.
4 th	04.03.23	1	2.3 Instruction Set of 8085(Data Transfer)
	06.03.23	1	2.3 Instruction Set of 8085(Arithmetic)
	09.03.23	1	2.3 Instruction Set of 8085(Logical, Branching)

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5 th	13.03.23	1	2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction
	14.03.23	1	2.4.2 Logic Operations (AND, OR, Complement 1's & 2's) & Masking of bits.
	15.03.23	1	2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register) 2.4.4 Looping, Counting & Indexing (Call/JMP etc)
	16.03.23	1	2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register)
	18.03.23	1	2.4.4 Looping, Counting & Indexing (Call/JMP etc)
6 th	20.03.23	1	2.4.5 Stack & Sub routines programmes.
	21.03.23	1	2.4.6 Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer.
	22.03.23	1	2.4.7 Compare between two numbers. 2.4.8 Array Handling (Largest number & smallest number in the array)
	23.03.23	1	2.5 Memory & I/O Addressing,
	25.03.23	1	Class Test 1
7 th	27.03.23	1	Unit-3: TIMING DIAGRAMS. 3.1 Define Opcode , operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle
	28.03.23	1	3.2 Draw timing diagram for memory read, memory write
	29.03.23	1	3.2 Draw timing diagram for I/O read, I/O write machine cycle
	03.04.23	1	3.3 Draw a neat sketch for the timing diagram for 8085 Instruction (MOV, MVI instruction).
	04.04.23	1	3.3 Draw a neat sketch for the timing diagram for 8085 Instruction (LDA ,STA instruction).

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9 th	10.04.23	1	4.4 Concept of Address decoding for I/O devices
	11.04.23	1	4.5 Programmable Peripheral Interface: 8255
	12.04.23	1	4.6 ADC & DAC with Interfacing
	13.04.23	1	4.7 Interfacing Seven Segment Displays
	15.04.23	1	4.8 Generate square waves on all lines of 8255
10 th	17.04.23	1	4.9 Design Interface a traffic light control system using 8255.
	18.04.23	1	Assignment 2 Cum Doubt Clearing
	19.04.23	1	Unit-5 Microprocessor (Architecture and Programming-16 bit-8086) 5.1 Register Organisation of 8086
	20.04.23	1	5.2 Internal Architecture of 8086
	22.04.23	1	5.3 Signal Description of 8086
	24.04.23	1	INTERNAL TEST
11 th	25.04.23	1	INTERNAL TEST
	26.04.23	1	INTERNAL TEST
	27.04.23	1	5.4 General Bus Operation & Physical Memory Organisation
	29.04.23	1	5.5 Minimum Mode & Timings
	01.05.23	1	5.6 Maximum Mode & Timings
02.05.23	1	5.7 Interrupts and Interrupt Service Routines, Interrupt Cycle, Non-Maskable Interrupt, Maskable Interrupt	
			5.9 Simple Assembly language programming using 8086

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13 th	08.05.23	1	Class Test 2
	09.05.23	1	Unit-6 Microcontroller (Architecture and Programming-8 bit) 6.1 Distinguish between Microprocessor & Microcontroller
	10.05.23	1	6.2 8 bit & 16 bit microcontroller 6.3 CISC & RISC processor
	11.05.23	1	6.4 Architecture of 8051 Microcontroller
	13.05.23	1	6.5 Signal Description of 8051 Microcontrollers
14 th	15.05.23	1	6.6 Memory Organisation-RAM structure, SFR
	16.05.23	1	6.7 Registers, timers, interrupts of 8051 Microcontrollers
	17.05.23	1	6.7 Registers, timers, interrupts of 8051 Microcontrollers
	18.05.23	1	6.8 Addressing Modes of 8051
	20.05.23	1	6.9 Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions, JUMP, LOOP, CALL Instructions, I/O Port Programming
15 th	22.05.23	1	6.9 Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions, JUMP, LOOP, CALL Instructions, I/O Port Programming
	23.05.23	1	6.9 Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions, JUMP, LOOP, CALL Instructions, I/O Port Programming
			6.10 Interrupts, Timer & Counters
			6.11 Serial Communication
			6.12 Microcontroller Interrupts and Interfacing to 8255
			REVISION AND PERVERIOUS YEAR QUESTION DISCUSSION
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