

<b>LESSON PLAN:</b>		
Discipline: <b>IT</b>	Semester: <b>3rd</b>	Name of the Teaching Faculty: <b>Ms.Supriya Mishra</b>
Subject: <b>Computer System Architecture</b>	No. Of classes allotted per week: <b>4 periods per week (Mon, Tues, Wed&amp; Fri – 1 period each)</b>	Semester: <b>From Date: 01-08-2023 To 30-11-2023</b>
<b>WEEK</b>	<b>CLASS DAY</b>	<b>THEORY /PRACTICAL TOPICS</b>
1st	01-08-2023	<b>Interaction with new batch</b>
	02-08-2023	<b>Introduction to CSA&amp; Syllabus Discussion</b>
	04-08-2023	<b>1. Basic structure of computer hardware</b> 1.1 Basic Structure of computer hardware
2nd	07-08-2023	1.2 Functional Units
	08-08-2023	1.3 Computer components
	09-08-2023	1.4 Performance measures
	11-08-2023	1.5 Memory addressing (Part 1)
3rd	14-08-2023	1.5 Memory addressing(Part 2) & Operations
	16-08-2023	<b>Recap &amp; Discussion of Important Questions of Chapter 1</b> <b>2. Instructions &amp; instruction Sequencing</b> 2.1 Fundamentals to instructions
	18-08-2023	2.2 Operands 2.3 Op Codes
4th	21-08-2023	2.4 Instruction formats
	22-08-2023	2.5 Addressing Modes(Part 1)
	23-08-2023	2.5 Addressing Modes(Part 2)
	25-08-2023	<b>Recap &amp; Discussion of Important Questions of Chapter 2</b> <b>3. Processor System</b> 3.1 Register Files
5th	28-08-2023	3.2 Complete instruction execution (Part 1) • Fetch • Decode • Execution
	29-08-2023	3.2 Complete instruction execution (Part 2)
	01-09-2023	<b>Class Test-1 (Tentative)</b> <b>Assignment-1</b>
6th	04-09-2023	3.3 Hardware control 3.4 Micro program control
	05-09-2023	<b>Diff b/w Hardwired Control and Microprogrammed Control</b> <b>Recap &amp; Discussion of Important Questions of Chapter 3</b>
	08-09-2023	<b>4. Memory System</b> 4.1 Memory characteristics
7th	11-09-2023	4.2 Memory hierarchy
	12-09-2023	4.3 RAM and ROM organization (Part 1)
	13-09-2023	4.3 RAM and ROM organization (Part 2)
	15-09-2023	4.4 Interleaved Memory

8th	18-09-2023	4.5 Cache memory
	22-09-2023	4.6 Virtual memory
9th	25-09-2023	<b>Recap &amp; Discussion of Important Questions of Chapter 4</b>
	26-09-2023	<b>5. Input – Output System</b>
		5.1 Input - Output Interface
	27-09-2023	5.2 Modes of Data transfer 5.3 Programmed I/O Transfer
10th	03-10-2023	5.4 Interrupt driven I/O
	04-10-2023	5.5 DMA
	06-10-2023	5.6 I/O Processor <b>Recap &amp; Discussion of Important Questions of Chapter 5</b>
11th	09-10-2023	<b>Internal Assessment(To be decided by Institution)</b>
	10-10-2023	<b>Internal Assessment(To be decided by Institution)</b>
	11-10-2023	<b>6. I/O Interface &amp; Bus architecture</b>
		6.1 Bus and System Bus
	13-10-2023	6.2 Types of System Bus • Data Bus • Address Bus • Control Bus
12th	16-10-2023	6.3 Bus Structure
	17-10-2023	6.4 Basic Parameters of Bus design
	18-10-2023	6.5 SCSI <b>Assignment-2</b>
	20-10-2023	6.6 USB
13th	23-10-2023	Puja Holiday
	24-10-2023	
	25-10-2023	
	27-10-2023	
14th	30-10-2023	<b>Recap &amp; Discussion of Important Questions of Chapter 6</b>
	31-10-2023	<b>7. Parallel Processing</b> <b>Introduction to Parallel Processing</b> 7.1 Parallel Processing
	01-11-2023	<b>Class Test-2 (Tentative)</b> <b>Assignment-2</b>
	03-11-2023	7.2 Linear Pipeline
15th	06-11-2023	7.3 Multiprocessor
	07-11-2023	7.4 Flynn"s Classification
	08-11-2023	7.4 Flynn"s Classification (Part 2)
	10-11-2023	<b>Recap of Chapter 1</b>
16th	13-11-2023	<b>Recap of Chapter 2</b>
	14-11-2023	<b>Recap of Chapter 3</b>
	15-11-2023	<b>Recap of Chapter 4</b>
	17-11-2023	<b>Recap of Chapter 4 (Cache Memory)</b>

17th	20-11-2023	<b>Recap of Chapter 4 (Virtual Memory)</b>
	21-11-2023	<b>Recap of Chapter 5</b>
	22-11-2023	<b>Recap of Chapter 6</b>
	24-11-2023	<b>Recap of Chapter 7</b>
17th	28-11-2023	Discussion of Important Questions
	29-11-2023	Last Date for submission of Notes by students