BHUBANANANDA ORISSA SCHOOL OF ENGINEERING , CUTTACK

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING

LAB MANUAL

Year & Semester: 2Year, 3rd Semester

Subject Code /Name: PR-2 , Circuit Theory and Simulation Lab

TABLEOF CONTENTS

EXPERIMENT NO- 01

AIMOFTHEEXPERIMENT:-Measurement of equivalent resistance in series and parallel circuit

COMPONENTREQUIRED:-

Sl .No	Name of the Components	Specification	Quantity
1	Resistors	500Ω,680Ω,330Ω,390Ω,270Ω	Each of2
2	Multi meter	Digital	1
3	Software	Multisim14.1	As required

THEORY:-

SERIESCIRCUIT:-

- In electrical circuit is in series connected the current flowing through the conductor is constant but voltage is not constant and the resistance can be calculated by,

$$R_s = R_1 + R_2 + R_3 + \dots\dots\dots\dots + R_n$$

PARALLELCIRCUIT:-

- In electrical series is in parallel connected the current flowing through the conductor is not constant but voltage is remain constant and the resistance can be calculated by,

$$\frac{1}{R_P} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + \frac{1}{R_4} + \dots\dots\dots\dots + \frac{1}{R_n}$$

PROCEDURE:-

1. Connected the resisted theres is to raspect circuit diagram.
2. Measured the individual resistance of different resistor with the help of multi meter.
3. Measured the total equivalent resistance as per circuit diagram by multi meter.
4. Compare the observed value and calculation value in both the parallel and series.

CALCULATION:-

Let two resistor are connected in series the n the total or equivalent resistor

is, R_1 = 330 Ω, R_2 = 390 Ω → $R_S = R_1 + R_2$ = 330 + 390 = 720 Ω

If, they are connected in parallel then the equivalent resistance is,

$R_1 = 330\ \Omega$, $R_2 = 390\ \Omega$

$$\frac{1}{R_P} = \frac{1}{330} + \frac{1}{390} \rightarrow R_P = \frac{330 \times 390}{330+390} = 178.8\Omega$$

OBSERVATIONTABLE:-

Sl No	R_1in (Ω)	R_2in (Ω)	RESISTANCEINSERIES		RESISTANCE INPARALLEL	
			Calculation	Observation	Calculation	Observation
1	500	680	1.18KΩ	1.18KΩ	293.2Ω	288.8Ω
2	270	680	950Ω	958Ω	197.7Ω	193.26Ω
3	330	390	721Ω	720Ω	118.5Ω	178.8Ω

CONCLUSION:-

From the above experiment it we have studied and verified that the observation value is approximately same to the calculation value in both parallel and series circuit.

EXPERIMENT NO- 02

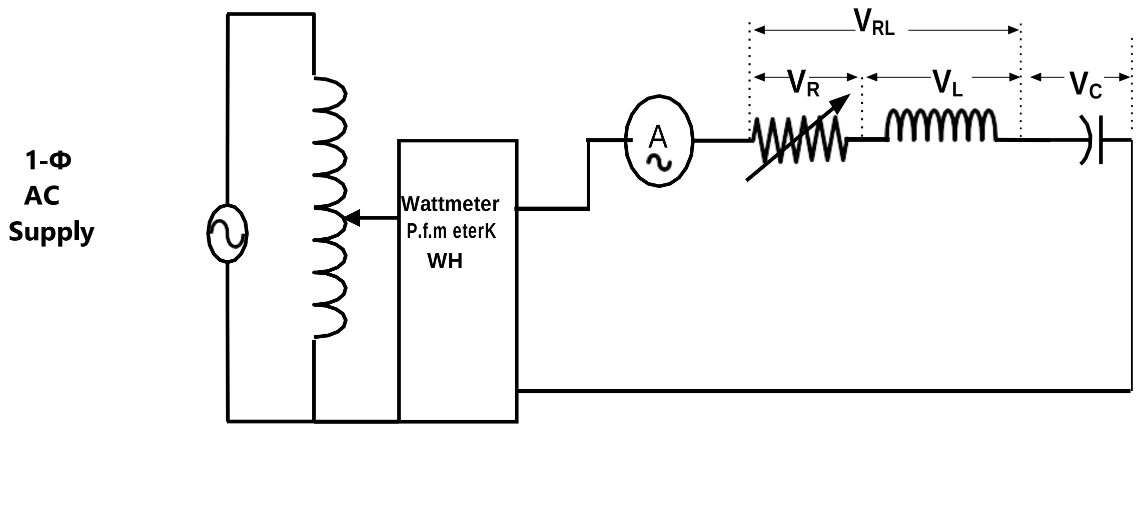
AIM OF THE EXPERIMENT - Measurement of power and power factor using series R-L-C Load.

APPARATUS REQUIRED:-

Sl. No	Name of the Equipment	Specification	Quantity
1	Variable Resistor	0-100Ω	1 no
2	Inductor	40W, 250V	1 no
3	Capacitor	2.5μF	1 no
4	1-ΦDimmerSet	0-250v	1 no
5	Voltmeter	0-300v	3 nos
6	Ammeter	0-5A	1 no
7	1-Φ Wattmeter	250V, 1kW	1 no
8	Power factor Meter	250v,5A	1 no
9	Connecting Wires	-	As per required

Theory:-A series RLC circuit is one the resistor, inductor and capacitor are connected in series across a voltage supply. The resulting circuit is called **series RLC circuit.**

Circuit Diagram:-



Observation Table:-

Sl .no	Type of Load	Reading of Wattmeter	Reading of PF Meter
1	R		
2	L		
3	C		
4	R-L		
5	R-C		
6	L-C		
7	R-L-C		

Procedure:-

1- We should take all the tools & Instrument for this experiment.
2- Connect as per Circuit diagram.
3- Then switch ON the supply.
4- Take reading of wattmeter and PF meter.

Conclusion:-From the above experiment , we learnt about the measurement of power and power factor using series R-L-C Load.

EXPERIMENTNO–03

AIM OF THE EXPERIMENT:- Verification of KCL&KVL.

EQUIPMENT REQUIRED:-

Sl No	Name of the Components	Specification	Quantity
1	Verification Kit	(OMEGA-ETB-201)	1
2	Patch Cord	------------	As per required
3	Power Supply	0-12Volt	--------------
4	Multi meter	Digital meter	1

THEORY:-

- KCL states that the algebraic sum of all the current meeting at a point or junction is equal to zero.
- It can be stated that total in coming current at a point will be equal to the total out going current.
- For verification of KCL we consider the given circuit.

PROCEDURE: -

1. Connect the circuit as per the circuit diagram.
2. Vary the voltage to take 5 different reading.
3. Observe different ammeter reading for each input voltage.
4. Compare the reading with the total current following the ckt.

CALCULATION:-

$R_1 = 270\Omega$ $R_2 = 330\Omega$

$R_3 = 500\Omega$ $V = 9V$

$R_{eq} = (270 \| 330) + 500$

$= 148.5 + 500 = 648.5\Omega$

$I_3 = \frac{V}{R_{eq}} = \frac{9}{648.5} = 0.013A = 13mA$

$I_1 = \frac{(0.013)\times 330}{270+330} = 7.15mA$

$I_2 = \frac{(0.013)\times 270}{270+330} = 5.85mA$ ➔ $I_1 + I_2 = 7.15mA + 5.85mA = 13mA$

<u>OBSERBATIONTABLE</u>:-

Sl No	Input voltage	I_1(270Ω) In mA	I_2(330Ω) In mA	I_3(500Ω) In mA	I_1+I_2 in mA
1.	10V	8.53	6.95	15.49	15.48
2.	9V	7.60	6.2	13.9	13.8
3.	8V	6.84	5.58	12.44	12.42
4.	7V	6	4.89	10.91	10.89
5.	5V	4.30	3.51	7.82	7.81

KIRCHHOFF'SVOLTAGELAW

THEORY:-

- KVL states that the algebraic sum of 'EMF' and product of current and resistance in a closed loop is equal to zero.
- For the verification of this theorem we have taken a circuit as shown in the figure.
- In the given circuit we have one 'EMF' and two resistance value 270 Ω and 330 Ω.
- The voltage across 270 Ω resistor is taken 'V_1'andacross330Ωresistoristaken'V_2'.

PROCEDURE: -

1. Connect circuit as per the circuit diagram.
2. Give the power apply to the circuit.
3. Now the measure the voltage across each resistor using Multi meter and note down the observe value in the observation table.
4. Now add all the three values of voltage obtain and compare it with the emf value.
5. This procedure may be respected for variable voltage values.

CALCULATION:-

Theoretically applying KVL to the given circuit, $V - IR_1 - IR_2 = 0$

$\Rightarrow 9 - I \times 270\Omega - I \times 330\Omega = 0$

$\Rightarrow 9 - I \times (270 + 330) = 0$

$\Rightarrow 9 = I \times (270 + 330)$ ➔ $I = \frac{9}{270+330} = 0.015A$ ➔

I =15mA

OBSERBATIONTABLE:-

SlNo	V_1(270 Ω)	V_2(330 Ω)	V_t(V_1+V_2)	TotalEMFApplied
1	4.57	5.70	10.27	10V
2	3.98	4.88	8.86	9V
3	3.22	3.91	7.13	7V
4	2.30	2.80	5.10	5V
5	0.90	1.10	2.00	2V

CONCLUSION:-

From the above experiment we observe that sum of emf and voltage drop is equal to zero.

EXPERIMENTNO–04

SUPERPOSITION THEOREM

AIM OF THE EXPERIMENT:- Verification of Super position theorem

EQUIPMENT REQUIRED:-

SL. NO	NAMEOFTHE COMPONENT	SPECIFICATION	QUANTITY
01	VERIFICATIONKIT	OMEGA TYPE-ETB201	01
02	POWERSUPPLY	0-12V	-
03	PATCHCORDS	**-**	As required
04	MULTIMETER	**-**	**-**

THEORY:-

In any linear bilateral network containing two or more independent sources (voltage or current sources or combination of voltage and current sources), the resultant current / voltage in any branch is the algebraic sum of currents / voltages caused by each independent source acting along, with all other independent sources being replaced meanwhile by their respective internal resistances.

The voltage source replaced by short circuit and the current circuit replaced by open circuit.Thevoltagesourcereplacedbyshorttheresistancesofthesourcearereplaced at the time of source elimination.

If the current produced by one source is in one direction while that produced by the other is in the opposite direction through the same resistor, the resulting current is the difference of the two and has the direction of the larger current. If the individual currents are in the same direction, the resulting current is the sum of two and has the direction of either current..

The total power delivered to a resistive element must be determined using the total current through or the total voltage across the element and cannot be determined by a simple sum of the power levels established by each source.

PROCEDURE: -

- Connect the power supply to the verification kit.
- Make the connection as per the circuit diagram.
- RemoveV_2andclosethecircuitthroughapatchcord.
- MeasureI_{31}intheammeter.
- Now put V_2in the circuit and remove V_1fromthe circuit. Close the circuit through a patch cord in place of V_1.
- NowreplaceandmeasureI_{32}inthe ammeter.
- NowreplaceV_1andswitchonboththesources.

CALCULATION:-

Now eliminate theV_2 Voltage from the circuit, $R_{eq} = R_1 + \frac{R2R3}{R2+R3}$

DuetovoltagesourceV$_1$, $I_{31} = I_1 \times \frac{R2}{R2+R3} = \frac{V1}{Req} \times \frac{R2}{R2+R3}$

Now eliminate theV1 voltage source from the circuit, $R_{eq} = R_2 + \frac{R1R3}{R1+R3}$

Due to voltage sourceV$_2$, $I_{32} = I_2 \times \frac{R1}{R1+R3} = \frac{V2}{Req} \times \frac{R1}{R1+R3}$

So, V_1= 9 V, V_2 = 5 V , R_1 = 270 Ω, R_2 = 330 Ω , R_3=390Ω,

R'_{eq} = 448.75 Ω, R''_{eq} = 489.54

So, $I_{31} = \frac{9}{448.75} \times \frac{330}{330+390}$ =9.19 mA

Similarly, $I_{32} = \frac{5}{489.54} \times \frac{270}{270+390}$

$I_3 = I_{31} + I_{32}$ =9.19+4.17=**13.36mA**

Loop-1 , $9 – 270I_1 – (I_1 – I_2) 390 = 0$

$9–270I_1– 390I_1+390I_2=0$

⇨ $660I_1–390I_2=9$..(i)

Loop-2,$-330I_2–5–(I_2-I_1)390=0$

⇨ $-330I_2–5-390I_2+390I_1=0$

⇨ $390I_1–720I_2=5$..(ii)

Now solving the equations (i) and (ii) we get,

I_1 = 0.014 = 14 m A , $I_2=6.5\times10^{-4}$=0.65mA So

, $I = I_1- I_2$= 14 – 0.65 = 13.35 m A

TABULATION:-

Sl No.	V_1 (Volt)	V_2 (Volt)	Total I_3 where both (V_1) and (V_2) active	Current where(V_1) active I_{31}	Current where(V_2) active I_{32}	Total ($I=I_{31}+I_{32}$)
01	9	5	13.04	8.97	4.09	13.06
02	9	7	14.65	8.97	5.65	14.26
03	9	9	16.29	8.97	7.30	16.27
04	9	10	17.28	8.97	8.12	17.09
05	9	11	17.98	8.97	8.97	17.64

CONCLUSION:-

From the above experiment we studied and observed that different branch current of the circuit using Super position theorem.

EXPERIMENTNO–05

VERIFICATION OF THEVENIN'S THEORM

AIM OF THE EXPERIMENT:-Verification of Thevenin's Theorem.

EQUIPMENTSREQUIRED

Sl No	Name of the Components	Specification	Quantity
1	Resistors	500Ω,680Ω,330Ω,270Ω	As Required
2	Multi meter	Digital	1
3	Connecting wire	------------------	-------
4	DC power supply	-------------------------	-------

THEORY

Any linear active 2 terminal n/w consisting in of voltage and current source with some resistance. It can be replaced by an equivalent Thevenin's voltage source or voltage source having its value equal to the Thevenin's equivalent voltage with a series resistor which is known as Thevenin's resistance. The equivalent voltage source is represented by 'V_{th}' and equivalent resistance is represented by 'R_{th}'. To find the Thevenin's equivalent voltage first we have to open circuit the load terminals. The open circuited voltage VAB is the required Thevenin's voltage. We have again equal to the voltage across the point 'P' and 'Q' so V_{PQ}=V_{AB}=V_{TH}.

PROCEDURE

1. Start–Electronics workbench –Multisim14.1
2. Select the component from place Component library according to given circuit diagram.
3. Connect the multi meter.
4. Make connection as per a circuit diagram.
5. Simulate Run.
6. Double click to the multi meter.
7. See the output result.

CALCULATION

R_1=300Ohm,R_2=500Ohm,R_3=680Ohm,R_L=9v

STEP-1

Calculate the V_{th} across load AB terminal and open the 270 ohm resistor.

$V_s = IR_1 + IR_2 = I(R_1 + R_2)$

$I = V_s/R_1 + R_2 = 9/330+500 = 1.08mA$

$V_{th} = V_{AB=} I \times 500 = 1.08 \times 500 = 5.42V$

STEP-2

Calculate the R_{th} across AB terminal by short circuit the voltage source.

$R_{ab} = (330||500) + 680 = \frac{330\times500}{330+500} + 680 = 878.795 Ohm$

Then find I_{th} in the Thevenin's equivalent circuit,

$I_{th} = \frac{Vth}{Rth+Rl} = \frac{5.422}{878.795+270} = 4.71mA$

I_l in find I will be equal to the I in thevenin's equivalent circuit.

TABULATION

CALCULATEDTABLE:-

Sl No.	Applied voltage in V	V_{th} In Volt	R_{th} in ohm	I_L In mA
01	09	5.42	878.795	4.72

OBSERVATIONTABLE:-

Sl. No	Applied voltage in volt	V_{th} in volt	R_{th} in ohm	I_L In mA
01	07	4.217	878.795	3.671
02	08	4.819	878.795	4.195
03	09	5.422	878.795	4.72
04	10	6.020	878.795	5.244
05	11	6.627	878.795	5.768
06	12	7.229	878.795	6.293

CONCLUSION

From the above experiment we know that how to verify Thevenin's theorem by using multisim14.1.

EXPERIMENTNO–06

VERIFICATION OF NORTON'S THEOREM

AIM OF THE EXPERIMENT:-Verification of Norton"s Theorem.

COMPONENT REQUIRED:-

Sl No	Name of the Components	Specification
1	Software	Multisim-14.1
2	Resistors	500Ω,680Ω,330Ω,270Ω
3	DC Power source	7Volt
4	Multi meter	As required

THEORY:-

- Inanylinearbilateralnetworkcontainingoneormorevoltagesourcecanbereplacedby an equivalent circuit.
- Consisting of current [I_N] in parallel with the equivalent resistance.
- In is the short circuited current following through the load terminals.

PROCEDURE:-

1. Start → Electronics work bench→Multisim14.1.
2. Select component from place →Component library according to following circuit.
3. Connect the multi meter.
4. Make connection according.
5. Simulate→ Run
6. Double click on the multi meter.
7. See the output result.

CALCULATION:-

STEP-1

First draws the given original circuit.

STEP-2

Assume load resistance as short circuited and calculate Norton's equivalent current as short circuited path.

Apply mesh analys is ,in loop 1 weget,➔$7-330I_1-500I_1+500I_2 = 0$

➔$7-830I_1+500I_2=0$ ➔ $830I_1-500I_2=7$ -------------------(1)

Apply mesh analys is ,in loop 2 we get,➔ -500I2+500I1-680I2=0

➔-1180I2 +500 I_1=0 ➔500I1-1180I2=0 -------------------(2)

By comparing or calculating Eq.1&Eq.2we get, I_1=0.011A=**11mA**&I_2=**4.79mA**

So, current across short circuit path or
Norton' s equivalent current[I_N]=**4.79mA**

STEP - 3

Assume load resistance as open circuit and find equivalent resistance or Norton's equivalent resistance,

R_N=(330‖500)+680

=198.795+680=**878.795Ω**

STEP-4

Now draw Norton' s equivalent circuit and find current across load resistance.

$I_L = \frac{4.79\times10^{-3}\times878.795}{878.795+270}$=**3.66mA**

TABULATION:-

Calculated Tabulation: -

Sl No	Applied voltage in (V)	I_N in (mA)	R_N in(mA)	I_L in(mA)
1	7V	4.798	878.795	3.67

Observation Tabulation:-

Sl No	Applied Voltage in(V)	I_N in (mA)	R_N in (mA)	I_L in (mA)
1	7	4.798	878.795	3.670
2	8	5.484	878.795	4.195
3	9	6.169	878.795	4.719
4	10	6.855	878.795	5.244
5	11	7.540	878.795	5.768
6	12	8.226	878.795	6.293

CONCLUSION:-

From the above experiment we know that how to verify the Norton's theorem by using software Multisim 14.1.

-

EXPERIMENTNO-07

VERIFICATION OF MAXIMUM POWER TRANSFER THEOREM

AIM OF THE EXPERIMENT:- To study& verify Maximum power transfer theorem.

COMPONENT REQUIRED:-

Sl No	Name of the Components	Specification
1	Software	Multisim-14.1
2	Resistors	1KΩ
3	Variable Resistor	10KΩ
4	DC Power source	12Volt
5	Multi meter	As required
6	Voltmeter	As required

THEORY:-

A resistive load being connected to a DC network receives maximum power when the load resistance is equal to the internal resistance of the source network as seen from load end.

EXPLANATION:-

A variable resistance 'R_L' is connected to a dc source network where 'V_0' represent the Thevenin's Voltage and 'R_{th}' represent the Thevenin's resistance of the source network. We have to find out the value of 'R_L' such that it receives the maximum from the dc source with reference to the fig the following can be written.

The current through the network 'I0' will be equal to mean , $I_O = \frac{V_O}{R_{TH}+R_L}$

The power delivered to the resistive load, $P_L=[I_O]^2 R_L = \frac{V_O^2 R_L}{(R_{TH}+R_L)^2}$

P_L can be maximized by varying the R_L & hence maximum power (P_{max}) can be delivered when, $\frac{d}{dR_L} P_L=0$

$$\Rightarrow \frac{d}{dR_L}\frac{V_O^2 R_L}{(R_{TH}+R_L)^2} = 0 \rightarrow V_O^2 \frac{d}{dR_L}\frac{R_L}{(R_{TH}+R_L)^2} = 0 \rightarrow V_O^2 \frac{(R_{TH}+R_L)^2 \frac{d}{dR_L}R_L - R_L \frac{d}{dR_L}(R_{TH}+R_L)^2}{(R_{TH}+R_L)^4} = 0$$

$$\Rightarrow \frac{V_O^2}{(R_{TH}+R_L)^4}(R_{TH}+R_L)^2 - R_L 2(R_{TH}+R_L)=0 \rightarrow (R_{TH}+R_L)[(R_{TH}+R_L)-2R_L]=0$$

$$\Rightarrow R_{TH}+R_L-2R_L=0 \rightarrow R_{TH}-R_L=0 \rightarrow R_{TH}=R_L \rightarrow \text{Thus } P_L = \frac{V_O^2 R_L}{(R_{TH}+R_L)^2}$$

By replacing $R_{TH}=R_L$ in this equitation , $P_L = \frac{V_O^2 R_L}{(2R_L)^2} \rightarrow P_L = \frac{V_O^2 R_L}{4R_L^2} \rightarrow P_L = \frac{V_O^2}{4R_L}$ $\quad P_{max} = \frac{V_O^2}{4R_L}$

PROCEDURE:-

1) Start→Electronicsworkbench→Multisim14.1.
2) Select component from place→ Component library according to following circuit.
3) Connect the multi meter.
4) Make connection according.

5) Simulate →Run
6) Double click on the multi meter.
7) See the output result

CALCULATION:-

STEP-1

First draws the given original circuit.

STEP-2

Assume load resistance as open circuited& calculate Thevenin's equivalent voltage as short circuited path

Apply mesh analysis, in loop we get,

$12-1000I_1-1000I_1=0 \Rightarrow 12-2000I_1=0$

$2000I_1=12 \Rightarrow \mathbf{I_1=6mA}$

So, voltage across open circuit path or Thevenin's equivalent voltage [V_{TH}] =6V

STEP-3

Assume load resistance as open circuit and find Thevenin's equivalent resistance,

$R_{TH}=(1000\parallel1000)+1000=500+1000=1500\Omega \Rightarrow \mathbf{R_{TH}=1.5K\Omega}$

STEP-4

After Thevenin's equivalent resistance found the P_{max} or maximum power of the circuit,

$\mathbf{P_{max}}=\frac{V_O^2}{4R_L}=\frac{6^2}{4\times1500}$ $\mathbf{P_{max}}$ **=6mW**

TABULATION

Sl No	Load resistance (R_L) (5KΩ)	V_{RL} in (V)	Po(V_{RL}/R_L) In mW
1	500(10%)	1.5	4.500
2	1000(20%)	2.4	5.760
3	**1500(30%)**	**3.0**	**6.000**
4	2000(40%)	3.4	5.870
5	2500(50%)	3.7	5.625

GRAPH

CONCLUSION:-

From the above experiment we concluded that for $R_L=R_{th}$ we get the maximum power transferred to the load end by using Multisim 14.1.

EXPERIMENTNO–08

RESONANCE CIRCUIT

AIM OF THE EXPERIMENT:-Determine resonant frequency of series R-L-C circuit

COMPONENT REQUIRED:-

Sl No	Name of the Components	Specification
1	Software	Multisim-14.1
2	Resistors	40kΩ
3	Capacitor	30mF
3	Inductor	0.1mH
4	Sine wave function generator	1kHZ,1Vpk

THEORY:-

SERIES RESONANCE:-

When an inductor and capacitor are connected in series the output current or voltage are maximum at a particular frequency depending on the values of inductor and capacitor.

This is called as resonance condition and the frequency is called resonating frequency at which the circuit attains resonance.

For a series L-C Resonant is given by, $F_o = \frac{1}{2\pi\sqrt{LC - \frac{R^2}{L^2}}}$

PROCEDURE:-

1) Start–ElectronicsWorkbench–Multisim14.1.
2) Select the components from place – Components library according to the following circuit
3) ConnectthePowersource[simulate–instrument-Powersource(A.CBattery)]
4) Simulate-Run

CONCLUSION:-

The circuit at resonance at particular frequency the frequency at which the amplitude get in creased.

EXPERIMENTNO-09

LOW-PASSFILTER

AIM OF THE EXPERIMENT:-Study of Low pass filter & determination of cut-off frequency.

COMPONENT REQUIRED:-

Sl No	Name of the Components	Specification
1	Software	Multisim-14.1
2	Resistors	1kΩ
3	Capacitor	1μF
3	Sine wave function generator	1kHZ,1Vpk
4	Bode Plotter	As required

THEORY:-

A low-pass filter is a filter that passes low- frequency signals but attenuates (reduces the amplitude of) signals with frequencies higher than the cutoff frequency.

The actual amount of attenuation for each frequency varies from filter to filter.

It is sometimes called a high-cut filter, or treble cut filter when used in audio applications.

A low-pass filter is the opposite of a high-pass filter , and a band- pass filter is a combination of a low-pass and a high-pass.

Low-pass filters exist in many different forms, including electronic circuits (such as a hiss filter used in audio), digital filters for smoothing sets of data, acoustic barriers, blurring of images, and so on.

The moving average operation used in fields such as finance is a particular kind of low-pass filter, and can be analyzed with the same signal processing techniques as are used for other low-pass filters.

Low-pass filters provide a smoother form of a signal, removing the short-term fluctuations, and leaving the longer-term trend.

In an electronic low-pass RC filter for voltage signals, high frequencies contained in the input signal are attenuated but the filter has little attenuation below its cutoff frequency which is $F_c = \frac{1}{2\pi R_c}$ determined by its RC time constant.

PROCEDURE:-

1. Start–Electronics Workbench–Multisim14.1.
2. Select the components from place–Components library according to following circuit.
3. Connect the Bode Plotter [Simulate–Instrument-Bode Plotter].
4. Simulate-Run.
5. Double click on Bode Plotter.

OBSERVATION:-

In the above experiment we observe that the output of CRO is much difference when the theoretical characteristic curve and the practical and theoretical curve are different.

CONCLUSION:-

From the above experiment we studied those characteristics of Low pass Filter by using software Multisim14.1.

EXPERIMENT NO –10

AIM OF THE EXPERIMENT

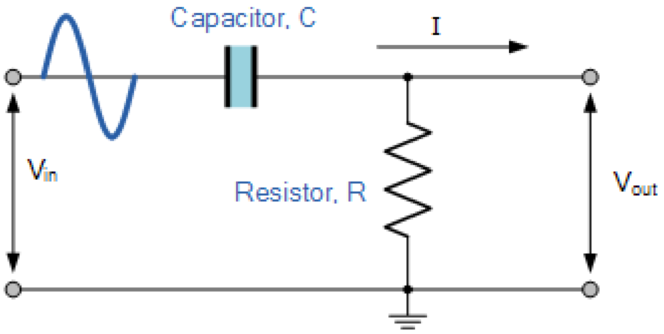
Study of High pass filter &determination of cut-off frequency

EQUIPMENT REQUIRED

SlNo	Name of the Components	Specification
1	Software	Multisim-14.1
2	Resistors	1kΩ
3	Capacitor	1µF
3	Sine wave function generator	1kHZ,1Vpk
4	Bode Plotter	As required

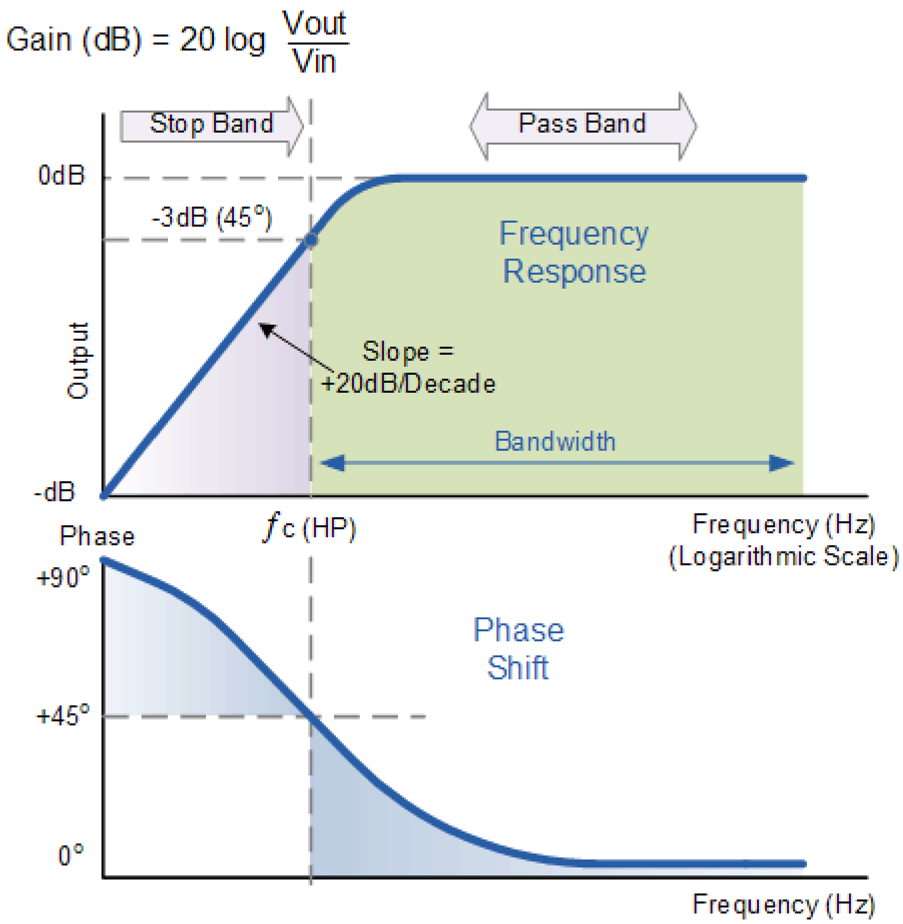
THEORY

The High Pass Filter Circuit



In this circuit arrangement, the reactance of the capacitor is very high at low frequencies so the capacitor acts like an open circuit and blocks any input signals at V_{IN} until the cut-off frequency point (f_C) is reached. Above this cut-off frequency point the reactance of the capacitor has reduced sufficiently asto now act more like a short circuit allowing all of the input signal to pass directly to the output as shown below in the filters response curve.

Frequency Response ofa1stOrderHighPass Filter



The **Bode Plot** or Frequency Response Curve above for a passive high pass filter is the exact oppositetothatofalowpassfilter.Herethesignalisattenuatedordampedatlowfrequencieswith the output increasing at +20dB/Decade (6dB/Octave) until the frequency reaches the cut-off point (ƒc) where again R = Xc. It has a response curve that extends down from infinity to the cut-off frequency ,where the output voltage amplitude is 1/√2=70.7%oftheinputsignalvalueor -3dB(20 log (Vout/Vin)) of the input value.

Also we can see that the phase angle (Φ)of the output signal **LEADS** that of the input and is equal to **+45°**at frequency ƒc. The frequency response curve for this filter implies that the filter can pass all signals out to infinity. However in practice, the filter response does not extend to infinity but is limited bythe electrical characteristics of the components used.

The cut-off frequency point for a first order highpass filter can be found using the same equation as that of the low pass filter, but the equation for the phase shift is modified slightly to account for the positive phase angle as shown below.

Cut-off Frequency and Phase Shift

$$fc = \frac{1}{2\pi RC}$$

$$Phase\ Shift\ \phi = arctan\ \frac{1}{2\pi fRC}$$

The circuit gain, Av which is given as Vout/Vin (magnitude)and is calculated as:

$$A_V = \frac{V_{OUT}}{V_{IN}} = \frac{R}{\sqrt{R^2 + Xc^2}} = \frac{R}{Z}$$

$$\text{at low } f: \ Xc \rightarrow \infty, \ \ Vout = 0$$
$$\text{at high } f: \ Xc \rightarrow 0, \ \ Vout = Vin$$

PROCEDURE

1. Start-Electronicworkbench-Multisim14.1.
2. Select the components from place-components library according to the following circuit.
3. Connect the oscilloscope [simulate-instrument-oscilloscope].
4. Simulate-Run.
5. Double click on the Bode plotter.

OBSERVATION

Intheaboveexperimentweobservethattheo/pofCROismuchdifferencewhenthe theoretical characteristics curve and the practical and theoretical curve are different.

CONCLUSION

From the above experiment we observe that the characteristics of high pass filter.