

***BHUBANANANDA ORISSA SCHOOL OF  
ENGINEERING, CUTTACK***

***ELECTRICAL ENGG. DEPARTMENT***

**LESSON PLAN**

**SEMESTER:- 5<sup>TH</sup> (C)**

**SESSION:- Winter(2022-23)**

**SUBJECT: Digital Electronics & Microprocessor**

**NAME OF FACULTY: PRIYANKA SAHU**

Discipline: <b>Electrical Engg.</b>	Semester: <b>5<sup>th</sup> (C)</b>	Name of the teaching faculty: <b>PRIYANKA SAHU</b>
Subject- <b>Digital Electronics &amp; Microprocessor</b>	No. of Days/per week class allotted: <b>05 PERIODS/WEEK (MON,TUE,WED,THU,FRI-1 Period Each)</b>	Semester: From Date: <b>15/09/2022</b> To Date: <b>22/12/2022</b> No. of weeks: <b>15 WEEKS</b>
Week	Class Day	Theory/Practical Topics
1 <sup>st</sup> (15/09/2022-17/09/2022)	15/09/2022	<b>1. Basics of Digital Electronics</b> 1.1 Binary, Octal, Hexadecimal number systems and compare with Decimal system.
	16/09/2022	1.1 Binary, Octal, Hexadecimal number systems and compare with Decimal system.
2 <sup>nd</sup> (19/09/2022-24/09/2022)	19/09/2022	1.1 Binary, Octal, Hexadecimal number systems and compare with Decimal system.
	20/09/2022	1.2 Binary addition, subtraction, Multiplication and Division.
	21/09/2022	1.3 1's complement and 2's complement numbers for a binary number.
	22/09/2022	1.3 1's complement and 2's complement numbers for a binary number.
	23/09/2022	1.3 1's complement and 2's complement numbers for a binary number.
3 <sup>rd</sup> (26/09/2022-01/10/2022)	26/09/2022	1.4 Subtraction of binary numbers in 2's complement method.
	27/09/2022	1.5 Use of weighted and Un-weighted codes & write Binary equivalent number for a number in 8421, Excess-3 and Gray Code and vice-versa.
	28/09/2022	1.5 Use of weighted and Un-weighted codes & write Binary equivalent number for a number in 8421, Excess-3 and Gray Code and vice-versa.
	29/09/2022	1.6 Importance of parity Bit.
	30/09/2022	1.6 Importance of parity Bit.

4 <sup>th</sup> (03/10/2022-08/10/2022)		<b>PUJA HOLIDAYS</b>
5 <sup>th</sup> (10/10/2022-15/10/2022)	10/10/2022	1.7 Logic Gates: AND, OR, NOT, NAND, NOR and EX-OR gates with truth table.
	11/10/2022	1.7 Logic Gates: AND, OR, NOT, NAND, NOR and EX-OR gates with truth table.
	12/10/2022	1.8 Realize AND, OR, NOT operations using NAND, NOR gates.
	13/10/2022	1.8 Realize AND, OR, NOT operations using NAND, NOR gates.
	14/10/2022	1.9 Different postulates and De-Morgan's theorems in Boolean algebra.
6 <sup>th</sup> (17/10/2022-22/10/2022)	17/10/2022	1.10 Use Of Boolean Algebra For Simplification Of Logic Expression
	18/10/2022	1.11 Karnaugh Map For 2,3,4 Variable, Simplification Of SOP And POS Logic Expression Using K-Map.
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	20/10/2022	1.11 Karnaugh Map For 2,3,4 Variable, Simplification Of SOP And POS Logic Expression Using K-Map.
	21/10/2022	<b>Class test 1</b>
7 <sup>th</sup> (24/10/2022-29/10/2022)	24/10/2022	<b>Kali Puja/Diwali</b>
	25/10/2022	<b>2. Combinational Logic Circuits</b>
	26/10/2022	2.1 Give the concept of combinational logic circuits.
	27/10/2022	2.2 Half adder circuit and verify its functionality using truth table.
	28/10/2022	2.3 Realize a Half-adder using NAND gates only and NOR gates only.
	28/10/2022	2.3 Realize a Half-adder using NAND gates only and NOR gates only.
8 <sup>th</sup> (31/10/2022-05/11/2022)	31/10/2022	2.4 Full adder circuit and explain its operation with truth table
	01/11/2022	2.5 Realize full-adder using two Half-adders and an OR – gate and write truth table.
	02/11/2022	2.6 Full subtractor circuit and explain its operation with truth table
	03/11/2022	2.6 Full subtractor circuit and explain its operation with truth table.
	04/11/2022	2.7 Operation of 4 X 1 Multiplexers and 1 X 4 demultiplexer.

9 <sup>th</sup> (07/11/2022-12/11/2022)	07/11/2022	2.7 Operation of 4 X 1 Multiplexers and 1 X 4 demultiplexer.
	08/11/2022	<b>Rasa Purnima</b>
	09/11/2022	2.8 Working of Binary-Decimal Encoder & 3 X 8 Decoder.
	10/11/2022	2.8 Working of Binary-Decimal Encoder & 3 X 8 Decoder.
	11/11/2022	2.9 Working of Two bit magnitude comparator.
10 <sup>th</sup> (14/11/2022-19/11/2022)	14/11/2022	<b>3. Sequential Logic Circuits</b>
	15/11/2022	3.1 Give the idea of Sequential logic circuits.
	16/11/2022	3.2 State the necessity of clock and give the concept of level clocking and edge triggering.
	17/11/2022	3.3 Clocked SR flip flop with preset and clear inputs.
	18/11/2022	<b>Internal Assessment</b>
	19/11/2022	3.4 Construct level clocked JK flip flop using S-R flip-flop and explain with truth table.
	20/11/2022	3.5 Concept of race around condition and study of master slave JK flip flop.
11 <sup>th</sup> (21/11/2022-26/11/2022)	21/11/2022	3.6 Give the truth tables of edge triggered D and T flip flops and draw their symbols
	22/11/2022	3.7 Applications of flip flops
	23/11/2022	3.8 Define modulus of a counter.
	24/11/2022	3.9 4-bit asynchronous counter and its timing diagram
	25/11/2022	3.10 Asynchronous decade counter
	26/11/2022	3.11 4-bit synchronous counter
	27/11/2022	3.12 Distinguish between synchronous and asynchronous counters
	28/11/2022	3.13 State the need for a Register and list the four types of registers
	29/11/2022	3.14 Working of SISO, SIPO, PISO, PIPO Register with truth table using flip flop
12 <sup>th</sup> (28/11/2022-03/12/2022)	28/11/2022	3.14 Working of SISO, SIPO, PISO, PIPO Register with truth table using flip flop
	29/11/2022	3.14 Working of SISO, SIPO, PISO, PIPO Register with truth table using flip flop
	30/11/2022	<b>Quiz test</b>
		<b>4. 8085 Microprocessor</b>
		4.1 Introduction to Microprocessors, Microcomputers

	01/12/2022	4.2 Architecture of Intel 8085A Microprocessor and description of each block
	02/12/2022	4.2 Architecture of Intel 8085A Microprocessor and description of each block
13 <sup>th</sup> (05/12/2022-10/12/2022)	05/12/2022	4.3 Pin diagram and description 4.3 Pin diagram and description
	06/12/2022	4.4 Stack, Stack pointer & stack top 4.5 Interrupts
	07/12/2022	4.6 Opcode & Operand 4.7 Differentiate between one byte, two byte & three byte instruction with example
	08/12/2022	4.8 Instruction set of 8085 example
	09/12/2022	4.9 Addressing modes 4.10 Fetch Cycle, Machine Cycle, Instruction Cycle, T-State
14 <sup>th</sup> (12/12/2022-17/12/2022)	12/12/2022	4.11 Timing Diagram for memory read, memory write, I/O read, I/O write
	13/12/2022	4.12 Timing Diagram for 8085 instruction`
	14/12/2022	4.13 Counter and time delay
	15/12/2022	4.14 Simple assembly language programming of 8085
	16/12/2022	<b>Class test 2</b>
15 <sup>th</sup> (19/12/2022-22/12/2022)	19/12/2022	<b>5. INTERFACING AND SUPPORT CHIPS</b> 5.1 Basic Interfacing Concepts 5.2 Memory mapping & I/O mapping Functional block diagram and description of each block of Programmable peripheral interface Intel 8255
	20/12/2022	5.2 Memory mapping & I/O mapping Functional block diagram and description of each block of Programmable peripheral interface Intel 8255
	21/12/2022	5.3 Application using 8255: Seven segment LED display, Square wave generator, Traffic light Controller
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		<b>REVISION</b>